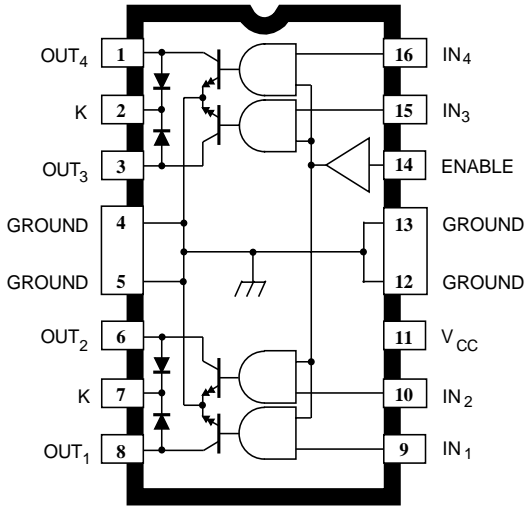


2540

QUAD DARLINGTON POWER DRIVER

UDN2540B (DIP)



Dwg. PP-017-2

Combining AND logic gates and inverting high-current bipolar outputs, the UDN2540B and A2540SLB quad Darlington power drivers provide interface between low-level signal-processing circuits and power loads totaling 360 W. Each of the four independent outputs can sink up to 1.8 A in the on state with peak inrush currents to 2.5 A. The four power outputs are each comprised of an open-collector Darlington driver and an internal flyback/clamp diode for switching inductive loads. They feature a minimum breakdown and sustaining voltage of 50 V. The logic inputs are compatible with TTL and 5 V CMOS logic systems.

Typical applications include print heads, relays, solenoids, and dc stepping motors. These drivers can also be used to drive high-current incandescent lamps, LEDs, and heaters.

The UDN2540B is supplied in a 16-pin batwing power DIP; the A2540SLB is supplied in a 20-lead batwing power SOIC for surface-mount applications. The batwing construction provides for maximum package power dissipation in a standard construction. At 25°C, and with pcb copper foil heat dissipators at the ground tabs, either package is capable of safely dissipating more than 2 W.

ABSOLUTE MAXIMUM RATINGS

at $T_A = 25^\circ\text{C}$

| | |
|---|------------------------|
| Output Voltage, V_{OUT} | 50 V |
| Output Current, I_{OUT} (peak) | 2.5 A |
| (continuous) | 1.8 A |
| Logic Supply Voltage, V_{CC} | 7.0 V |
| Input Voltage, V_{IN} | 7.0 V |
| Package Power Dissipation, P_D | See Graph |
| Operating Temperature Range, T_A | -20°C to +85°C |
| Storage Temperature Range, T_S | -55°C to +150°C |

FEATURES

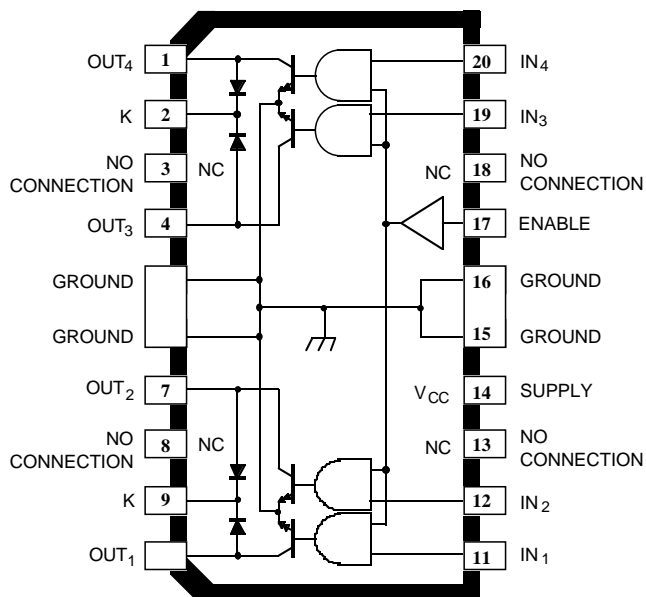
- 1.8 A Continuous Output Current
- Output Voltage to 50 V
- TTL and 5 V CMOS Compatible Inputs
- Efficient Input/Output Pinning
- Integral Transient-Suppression Diodes
- Replaces L6221A and L6221CD

Always order by complete part number:

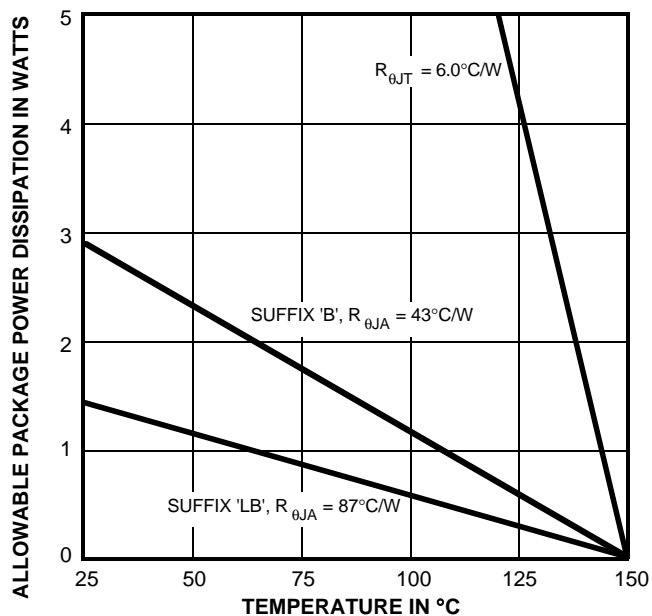
| Part Number | Package |
|-------------|----------------------|
| UDN2540B | 16-pin batwing DIP |
| A2540SLB | 20-lead batwing SOIC |

2540 QUAD DARLINGTON POWER DRIVER

A2540SLB (SOIC)



Dwg. PP-017-3



Dwg. GP-049-3A

$R_{\theta JA}$ is measured on typical two-sided PCB with minimal copper ground area. For the SOIC, adding 3.57 in² copper ground area will reduce the thermal resistance to 52°C/W (2.4 W allowable package power dissipation at 25°C). See Application Note 29501.5, *Improving Batwing Power Dissipation*.

TRUTH TABLE

| ENABLE | IN _N | OUT _N |
|--------|-----------------|------------------|
| H | H | ON |
| — | L | OFF |
| L | X | OFF |

X = Don't care.

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ELECTRICAL CHARACTERISTICS at $T_A = 25^\circ\text{C}$, $T_J \leq 150^\circ\text{C}$, $V_{CC} = 4.75\text{ V to }5.25\text{ V}$.

| Characteristic | Symbol | Test Conditions | Limits | | | Units |
|-----------------------------|---------------|--|--------|------|------|---------------|
| | | | Min. | Typ. | Max. | |
| Output Leakage Current | I_{CEX} | $V_{OUT} = 50\text{ V}$, $V_{IN} = 0.8\text{ V}$, $V_{EN} = 2.4\text{ V}$ | — | <1.0 | 100 | μA |
| | | $V_{OUT} = 50\text{ V}$, $V_{IN} = 2.4\text{ V}$, $V_{EN} = 0.8\text{ V}$ | — | <1.0 | 100 | μA |
| Output Sustaining Voltage | $V_{CE(sus)}$ | $I_{OUT} = 1.8\text{ A}$, $L = 3.0\text{ mH}$ | 50 | — | — | V |
| Output Saturation Voltage | $V_{CE(SAT)}$ | $I_{OUT} = 600\text{ mA}$, $V_{IN} = V_{EN} = 2.4\text{ V}$ | — | 0.9 | 1.0 | V |
| | | $I_{OUT} = 1.0\text{ A}$, $V_{IN} = V_{EN} = 2.4\text{ V}$ | — | 1.0 | 1.2 | V |
| | | $I_{OUT} = 1.8\text{ A}$, $V_{IN} = V_{EN} = 2.4\text{ V}$ | — | 1.3 | 1.6 | V |
| Input Voltage | Logic 1 | $V_{IN(1)}$ or $V_{EN(1)}$ | 2.4 | — | — | V |
| | Logic 0 | $V_{IN(0)}$ or $V_{EN(0)}$ | — | — | 0.8 | V |
| Input Current | Logic 1 | $V_{IN(1)}$ or $V_{EN(1)} = 2.4\text{ V}$ | — | — | 10 | μA |
| | Logic 0 | $V_{IN(0)}$ or $V_{EN(0)} = 0.8\text{ V}$ | — | — | -100 | μA |
| Total Supply Current | I_{CC} | $V_{IN}^* = V_{EN} = 2.4\text{ V}$, $V_{CC} = 5.0\text{ V}$, Outputs Open | — | 14 | 20 | mA |
| | | $V_{IN}^* = V_{EN} = 0.8\text{ V}$, $V_{CC} = 5.0\text{ V}$ | — | 0.4 | 2.0 | mA |
| Clamp Diode Forward Voltage | V_F | $I_F = 1.0\text{ A}$ | — | 1.3 | 1.6 | V |
| | | $I_F = 1.8\text{ A}$ | — | 1.6 | 2.0 | V |
| Clamp Diode Leakage Current | I_R | $V_R = 50\text{ V}$ | — | <1.0 | 100 | μA |

Typical Data is for design information only.

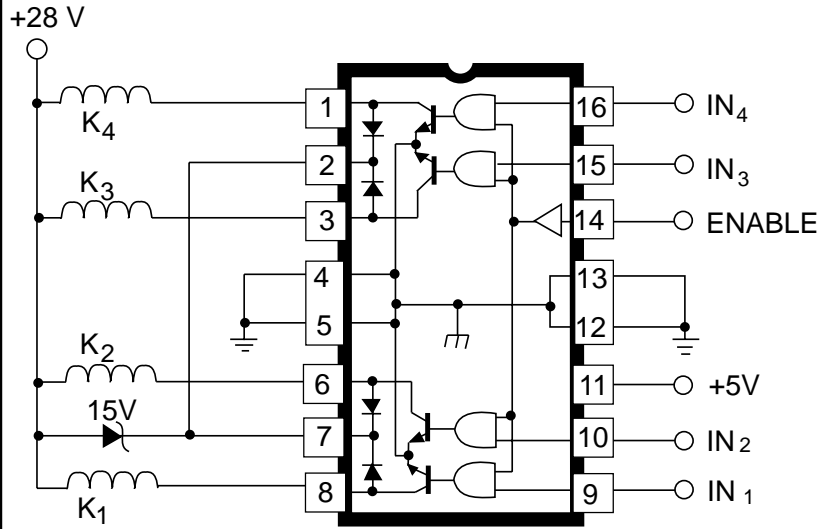
Negative current is defined as coming out of (sourcing) the specified terminal.

As used here, -100 is defined as greater than +10 (absolute magnitude convention) and the minimum is implicitly zero.

*All inputs simultaneously, all other tests are performed with each input tested separately.

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TYPICAL APPLICATION (QUAD RELAY DRIVER WITH ZENER FLYBACK)



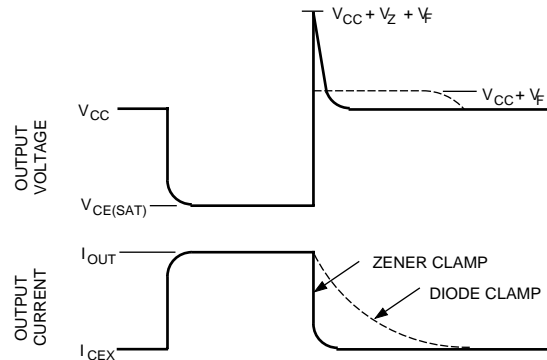
Dwg. EP-016

APPLICATIONS INFORMATION

A typical application is shown for driving four high-current relays, solenoids, or print heads. A Zener diode is used to increase the flyback voltage, providing a much faster inductive load turn-off current decay, resulting in faster dropout (reduced relay contact arcing), and improved performance. The maximum Zener voltage, plus the load supply voltage, plus the flyback diode forward voltage must not exceed the device's rated sustaining voltage.

With external control circuitry, the ENABLE input can be used for chopper (PWM) applications. If the ENABLE input is not used, it should be tied high.

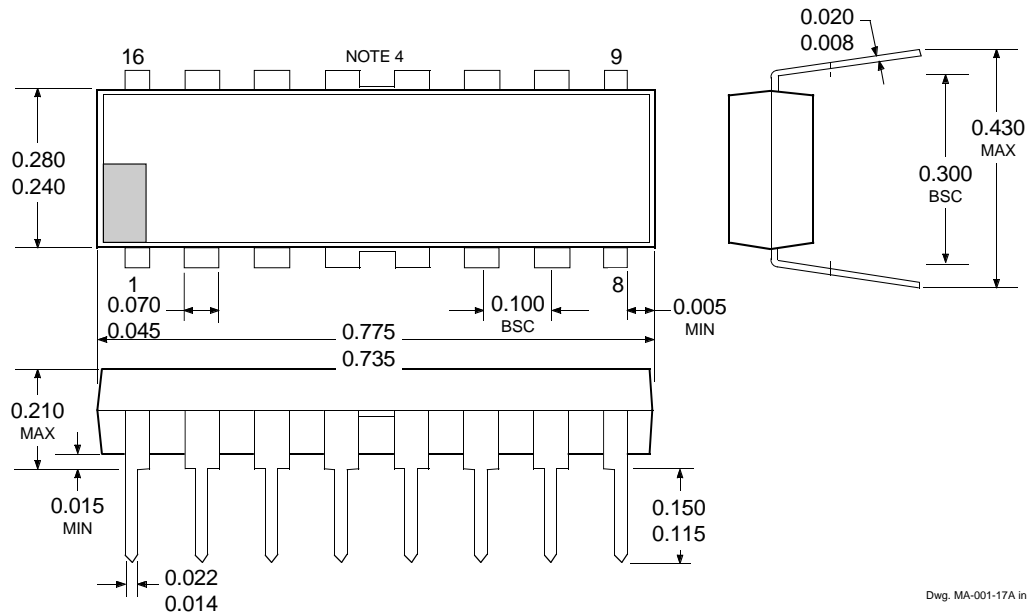
All inputs will float high if open circuited.



Dwg. WP-001

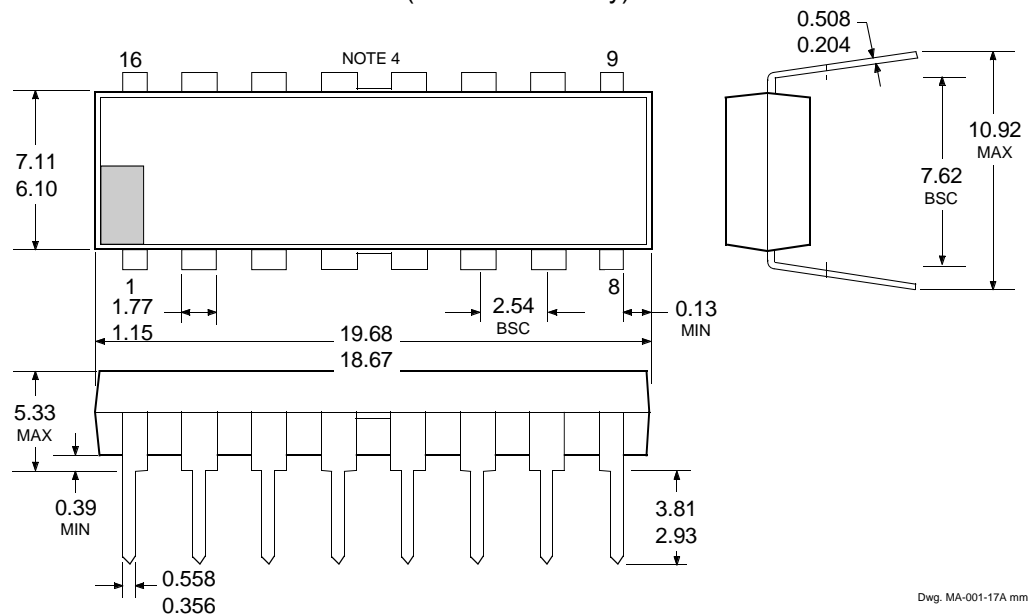
2540 QUAD DARLINGTON POWER DRIVER

UDN2540B Dimensions in Inches (controlling dimensions)



Dwg. MA-001-17A in

Dimensions in Millimeters (for reference only)

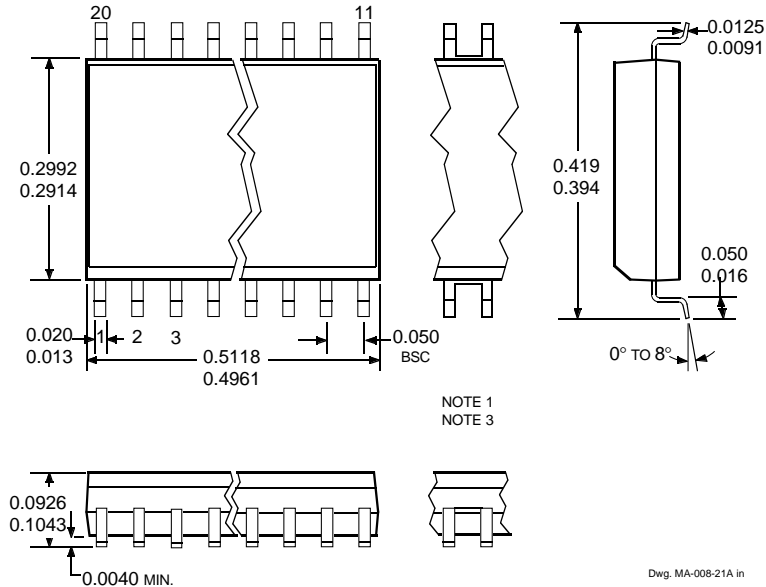


Dwg. MA-001-17A mm

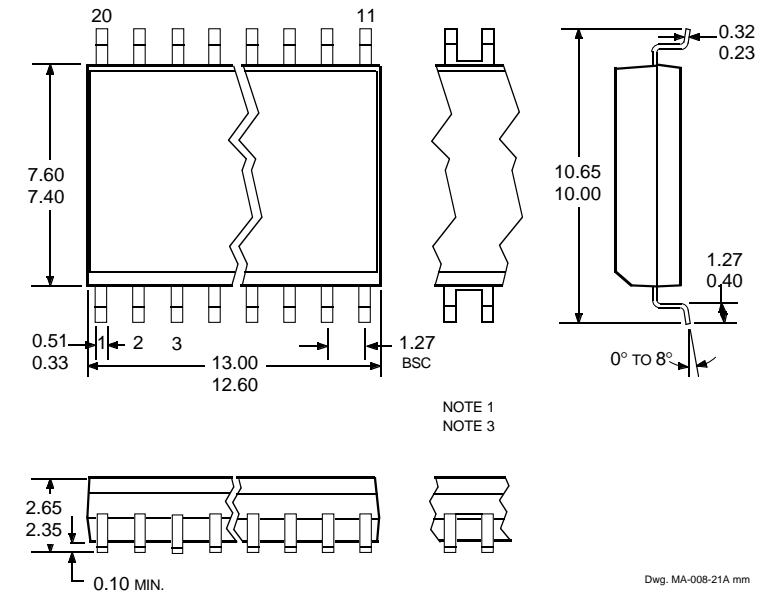
- NOTES: 1. Leads 1, 8, 9, and 16 may be half leads at vendor's option.
 2. Lead thickness is measured at seating plane or below.
 3. Lead spacing tolerance is non-cumulative.
 4. Webbed lead frame. Leads indicated are internally one piece.
 5. Exact body and lead configuration at vendor's option within limits shown.

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A2540SLB Dimensions in Inches (for reference only)



Dimensions in Millimeters (controlling dimensions)



- NOTES: 1. Webbed lead frame. Leads 5, 6, 15, and 16 are internally one piece.
2. Lead spacing tolerance is non-cumulative.
3. Exact body and lead configuration at vendor's option within limits shown.

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